

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
27 May 2004 (27.05.2004)

PCT

(10) International Publication Number
WO 2004/044764 A3

(51) International Patent Classification⁷: **G06F 15/78**,
H04L 25/02, H04J 3/06

[DE/DE]; c/o Philips Intellectual Property & Standards
GmbH, Weissshausstr. 2, 52066 Aachen (DE).

(21) International Application Number:
PCT/IB2003/004838

(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

(22) International Filing Date: 30 October 2003 (30.10.2003)

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE,
GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR,
KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK,
MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT,
RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR,
TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
102 52 165.4 9 November 2002 (09.11.2002) DE

(71) Applicant (*for DE only*): **PHILIPS INTELLECTUAL
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-
damm 94, 20099 Hamburg (DE).

(84) Designated States (*regional*): ARIPO patent (BW, GH,
GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE,
SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA,
GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(71) Applicant (*for all designated States except DE, US*):
KONINKLIJKE PHILIPS ELECTRONICS N.V.
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven
(NL).

(72) Inventor; and

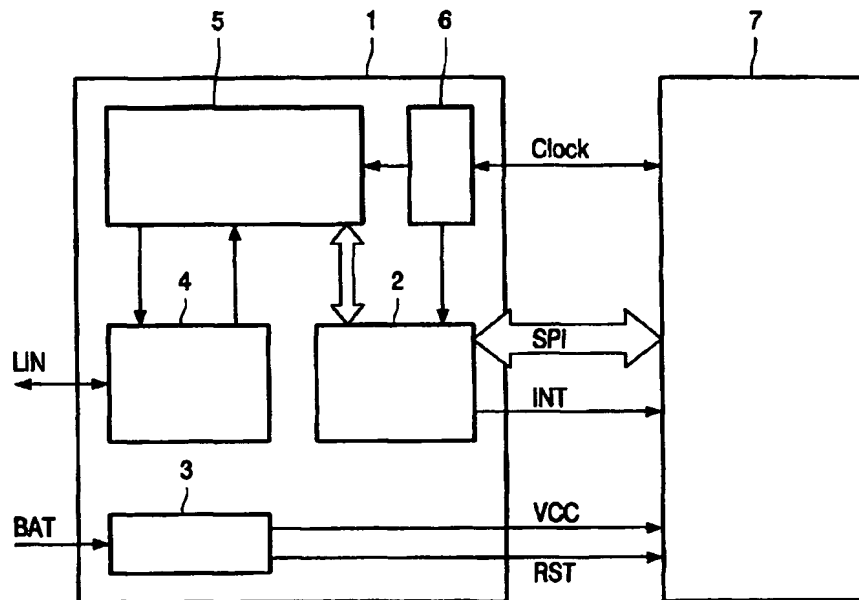
Published:

(75) Inventor/Applicant (*for US only*): **MUTH, Matthias**

— with international search report

[Continued on next page]

(54) Title: INTEGRATED CIRCUIT



(57) **Abstract:** An integrated circuit having-a system base chip (1) that has basic functions for a transmitting and/or receiving system for a vehicle data bus, namely at least a system voltage supply (3), a system reset (3) and a monitoring function (2),-an interface circuit (4, 5) that, in a self-contained fashion, runs at least parts of a data bus protocol, and in particular the LIN (Local Interconnect Network) protocol, that performs detection of the bit-rate of received data, and that is capable of passing on at least one received or transmitted byte.-a serial/parallel converter (5) that makes use in its conversion of the bit-rate detected by the interface circuit (4, 5).

WO 2004/044764 A3



— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(88) Date of publication of the international search report:
7 April 2005